

IN THE UNITED STATES PATENT AND TRADEMAR

In re application of: KELKAR et al.

Attorney Docket No.: NSC1P181/P0476

Application No.: 09/653,925

Examiner: Unknown

Filed: September 01, 2000

Group: 2811

Title: WAFER LEVEL CHIP SCALE PACKAGE

CERTIFICATE OF MAILING

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REQUEST FOR CORRECTED FILING RECEIPT

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Sir:

Enclosed is a copy of the Filing Receipt for the above-identified patent application with the following changes noted thereon:

Applicant(s)

ant(s)
Nikhil Vishwanath Kelkar, San Jose, SA; AVAILABLE COPY Hem P. Takiar, Fremont, CA;

Please reprint a Corrected Filing Receipt and mail the same to the undersigned.

Since this is a Patent Office typographical error, no fee should be due. However, the Commissioner is authorized to charge any fees that may be due to Deposit Account 50-0388 (Order No.NSC1P181).

Respectfully submitted,

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 APPLICATION NUMBER
 FILING DATE
 GRP ART UNIT
 FIL FEE REC'D
 ATTY DOCKET NO
 DRAWINGS
 TOT CLAIMS
 IND CLAIMS

 09/653,925
 09/01/2000
 2811
 690
 NSC1P181/P04767
 9
 20
 3

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FILING RECEIPT

OC000000005520253

Date Mailed: 10/31/2000

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Applicant(s)

Nikhil Vishwanath Kelkar, San Jose, SANADA; CA; Hem P. Takiar, Fremont, CA;

Continuing Data as Claimed by Applicant

Foreign Applications

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If Required, Foreign Filing License Granted 10/31/2000

Title

Wafer level chip scale package

Preliminary Class

257

Data entry by : MARTIN, DIANE Team : OIPE

Date: 10/31/2000

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